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SPECIFICATION

BUFFER MEMORY ADDRESS TRANSLATION DEVICE, SECTOR ADDRESS
INFORMATION RELIABILITY DETERMINATION DEVICE, DEFECTIVE
SECTOR DETERMINATION DEVICE, ECC BLOCK SYNCHRONIZATION
DETECTION DEVICE, OPTICAL DISK DRIVE, MEDIUM, AND PROGRAM

m05
11.30.05 This application is a 371 of PCT/JP02/04963 06/06/2001
Technical Field

The present invention relates to a buffer memory address translation device that generates a data storage address in a buffer memory used for an error correction processing of data read from an optical disk and data transfer to a host computer, a sector address reliability determination device that processes sector address information of data read from an optical disk, a defective sector determination device that determines a defective sector of an optical disk, an ECC block synchronization detection device that detects a synchronization of ECC blocks of data read from an optical disk, an optical disk drive, a medium, and an information aggregate.

Background Art

In a conventional optical disk drive, as disclosed in Japanese Patent Laid-Open No. 10-302402 and Japanese Patent Laid-Open No. 11-162105, generation of a data